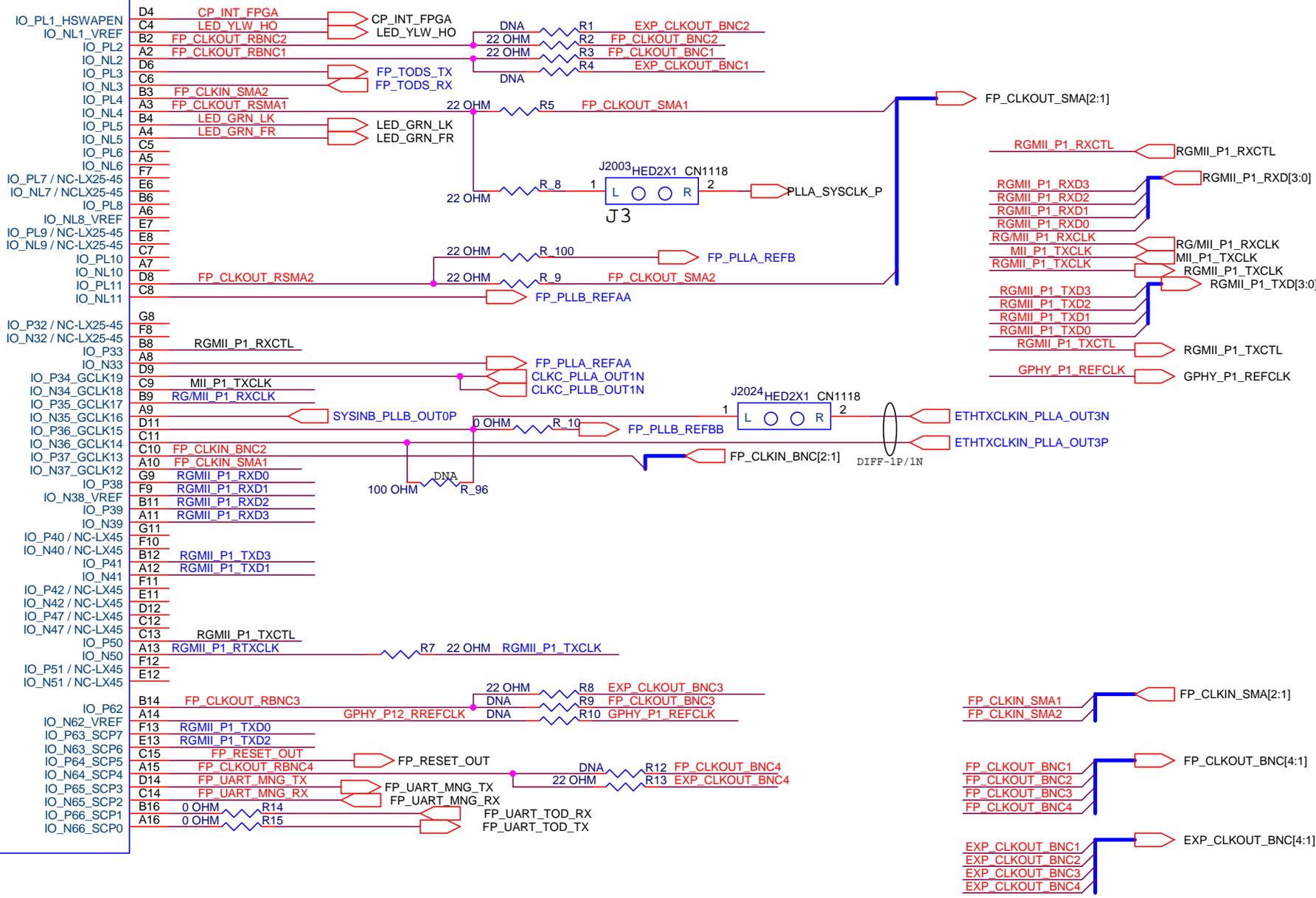


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Title	TOP			Rev	1.0
Size	A3	Document Number		AS00PC600	
Date:	Wednesday, March 26, 2014		Sheet	1	of 25

U1A **BANK-0**

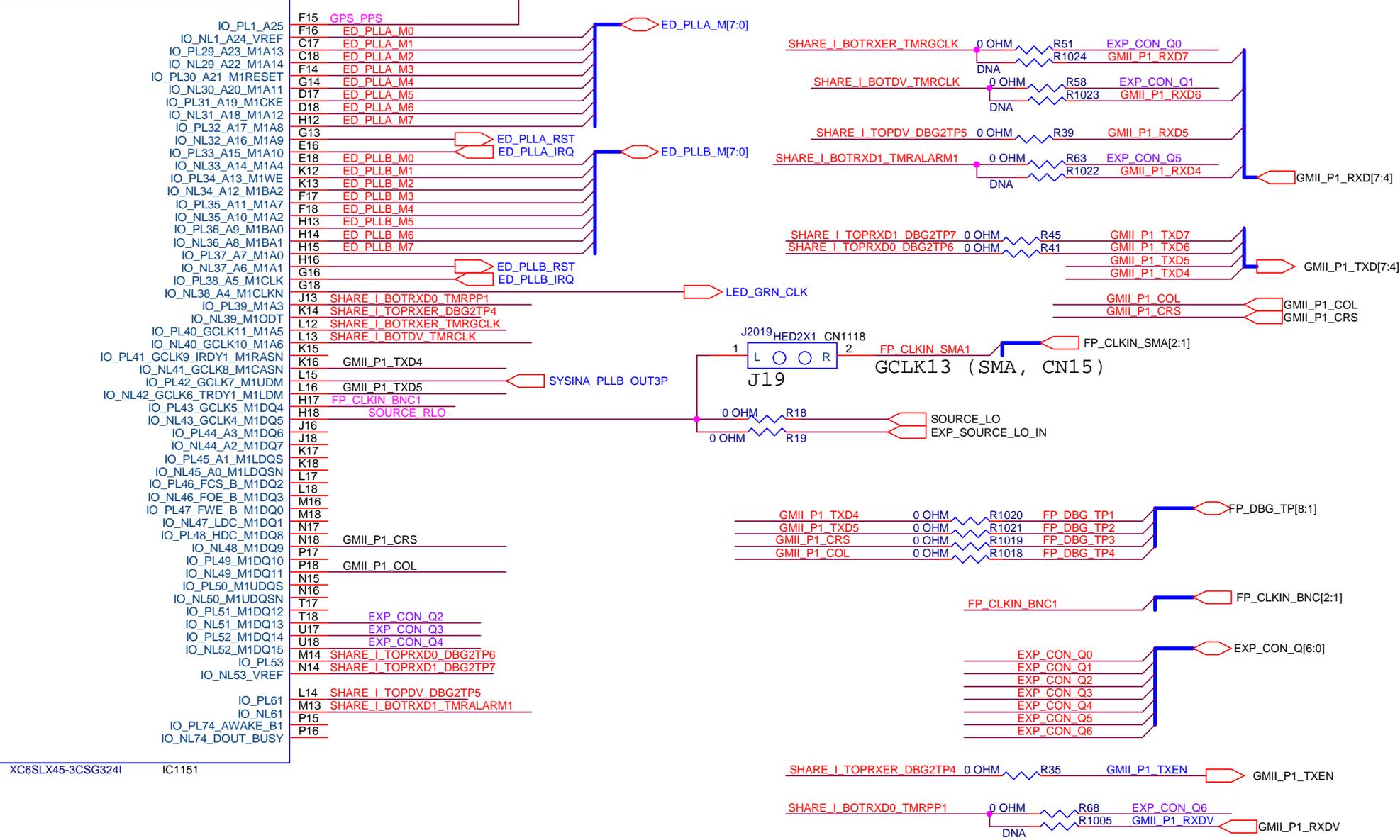


XC6SLX45-3CSG324I
IC1151

<Variant Name>			
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Size		Document Number	
A4		AS00IPC600	Rev 1.0
Date		Thursday, March 27, 2014	
Sheet		2 of 25	



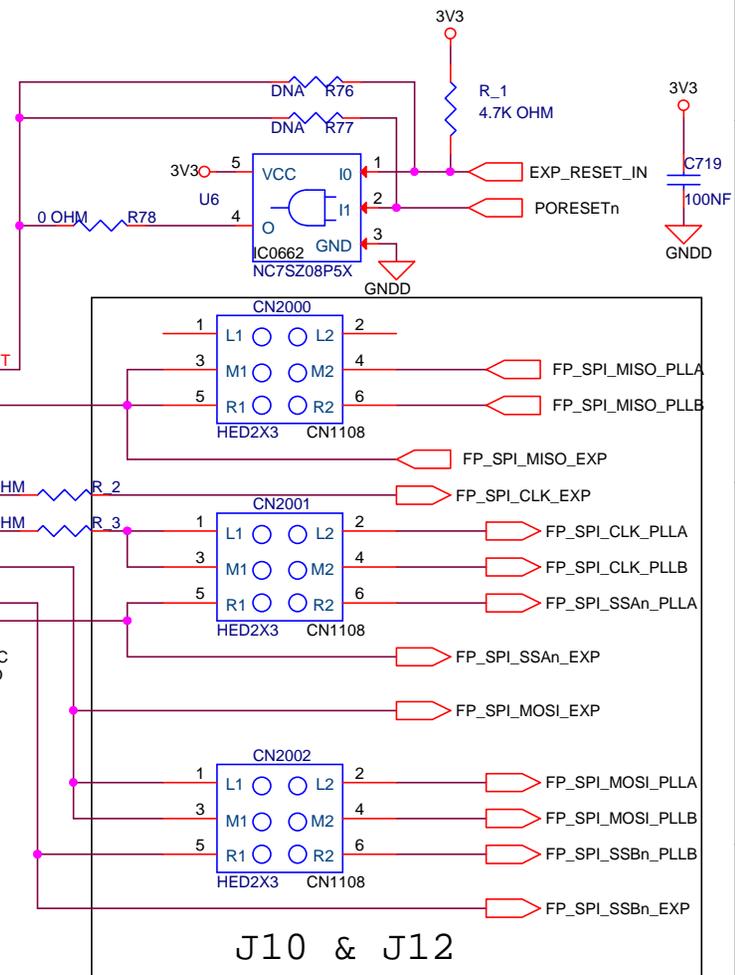
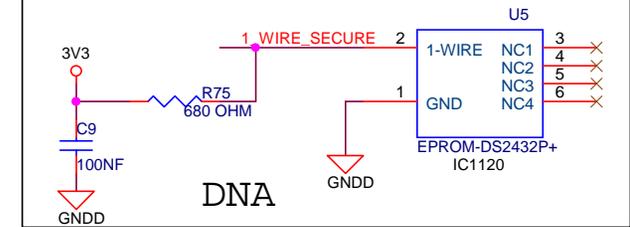
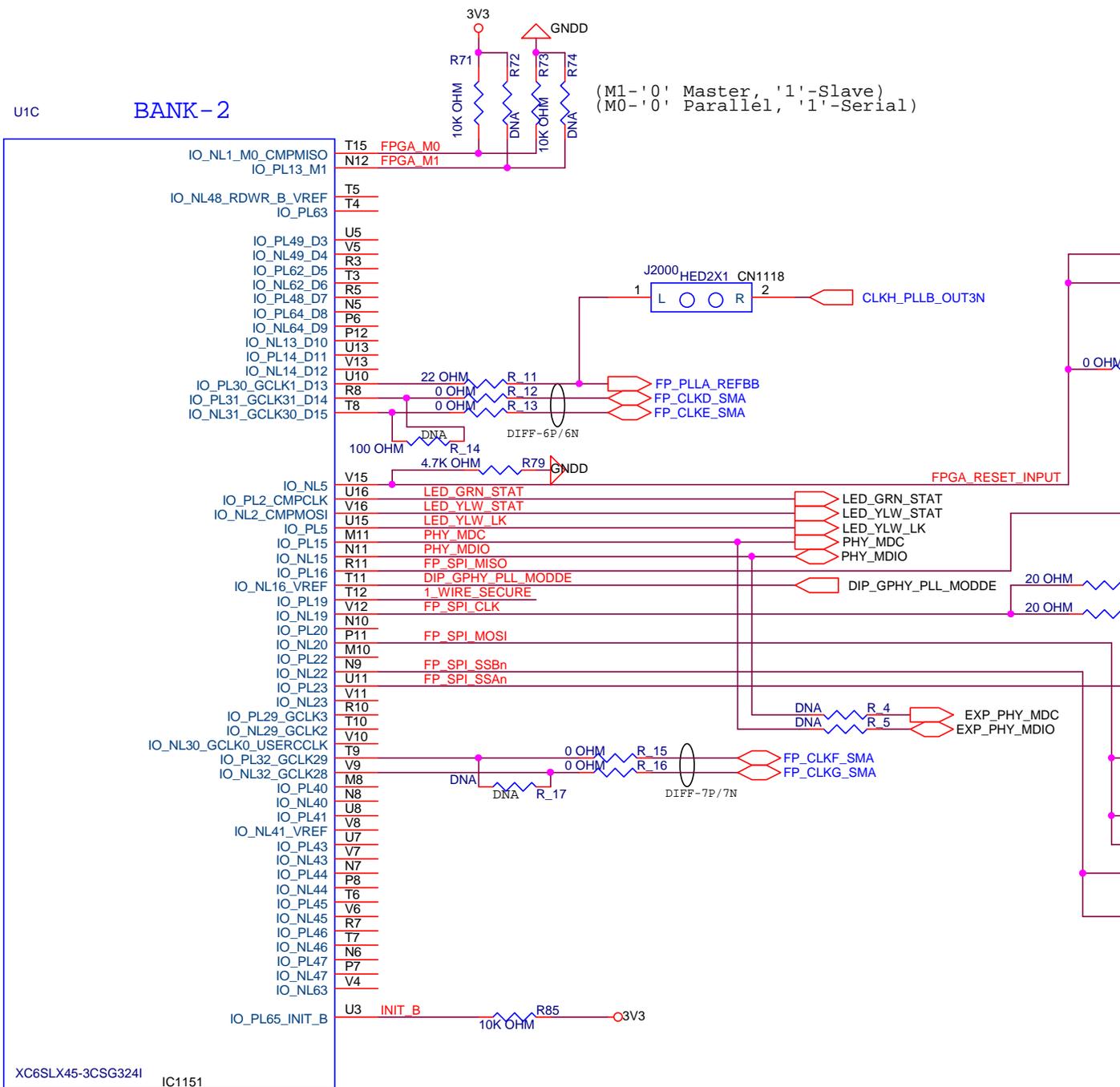
U1B BANK-1



XC6SLX45-3CSG324I IC1151

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Size	A4	Document Number	AS00IPC600		
Date:	Wednesday, March 26, 2014		Sheet	3	of 25

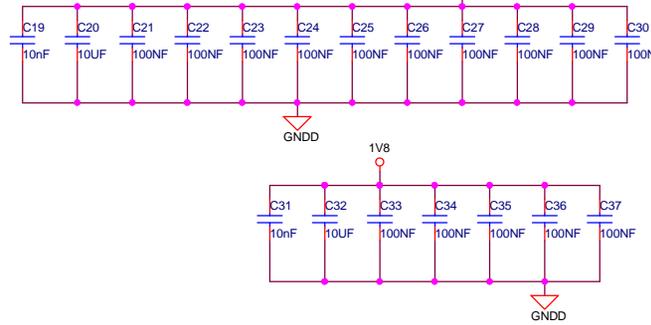
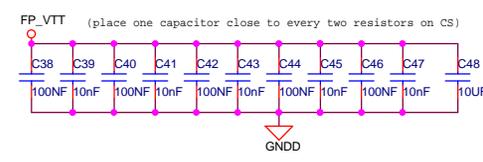
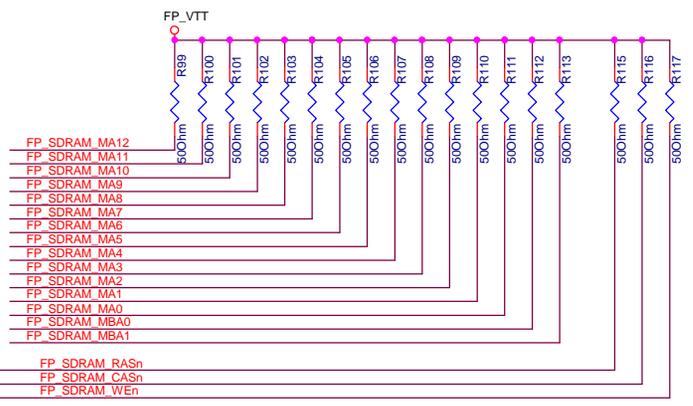
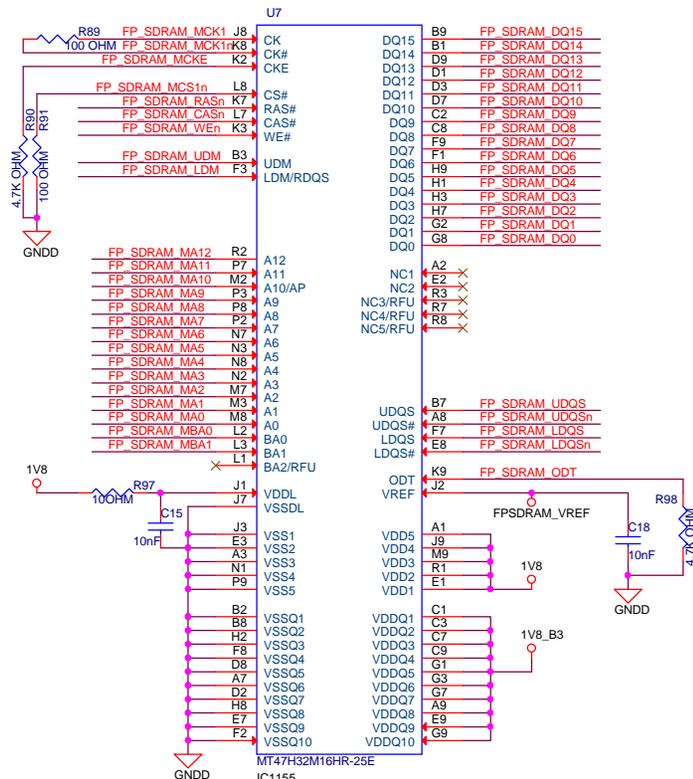
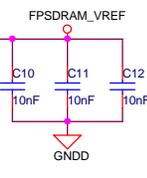
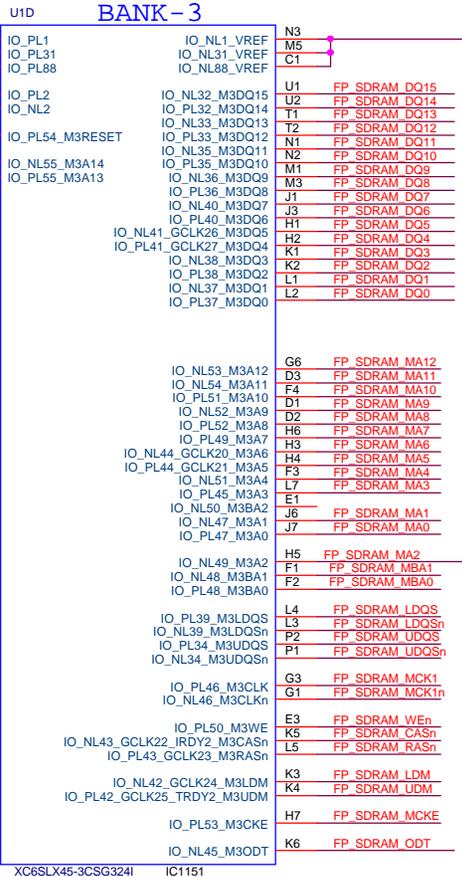


(Assemble low when need to delay configuration and GPIO control)

<Variant Name>			
Title		FPGA BANK-2	
Size		Document Number	
A4		AS00IPC600	Rev 1.0
Date		Monday, March 31, 2014	
Sheet		4 of 25	



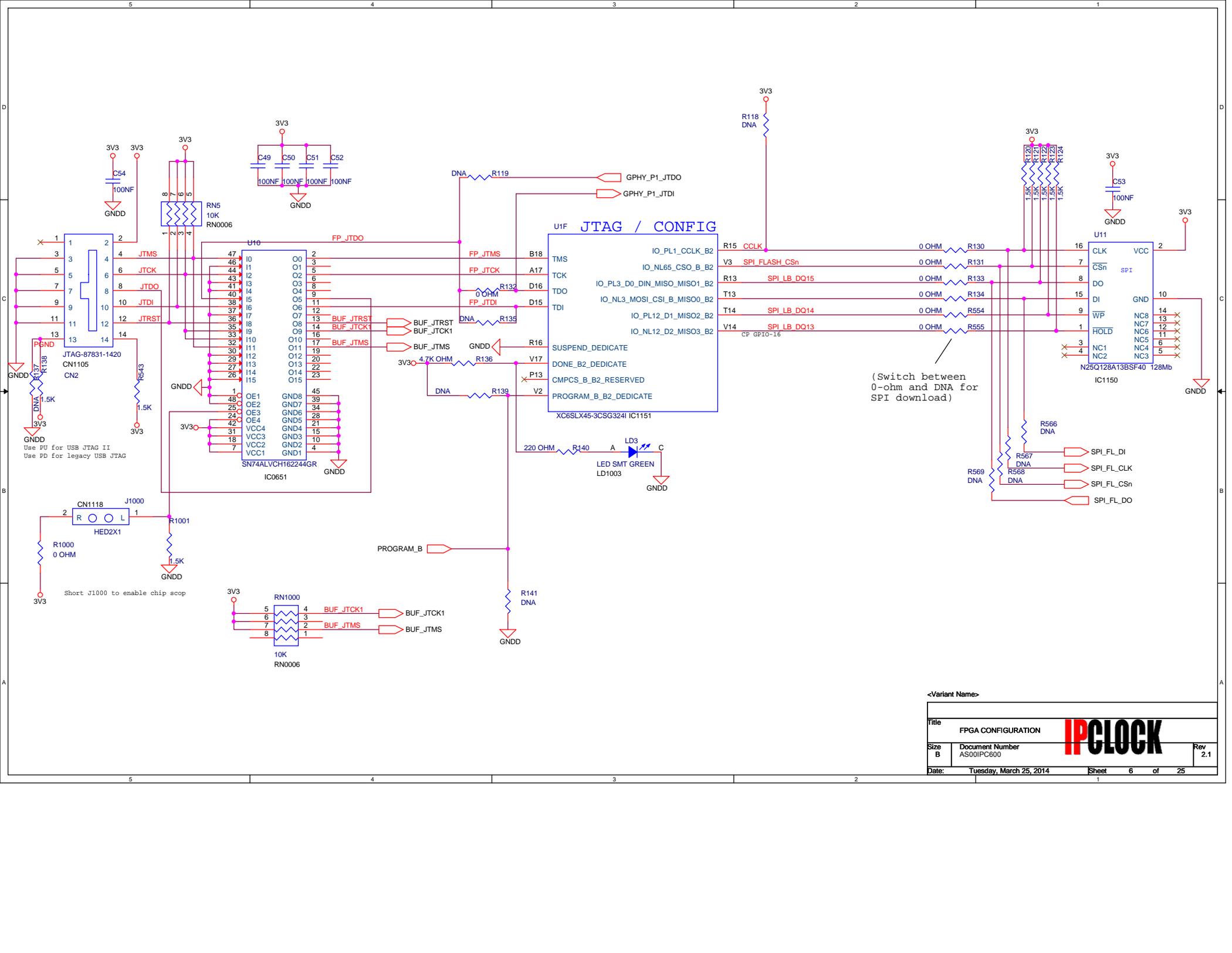
BANK-3



Title		FPGA BANK-3	
Size	B	Document Number	AS00IPC600
Date:	Tuesday, March 25, 2014	Sheet	5 of 25



Rev 1.0

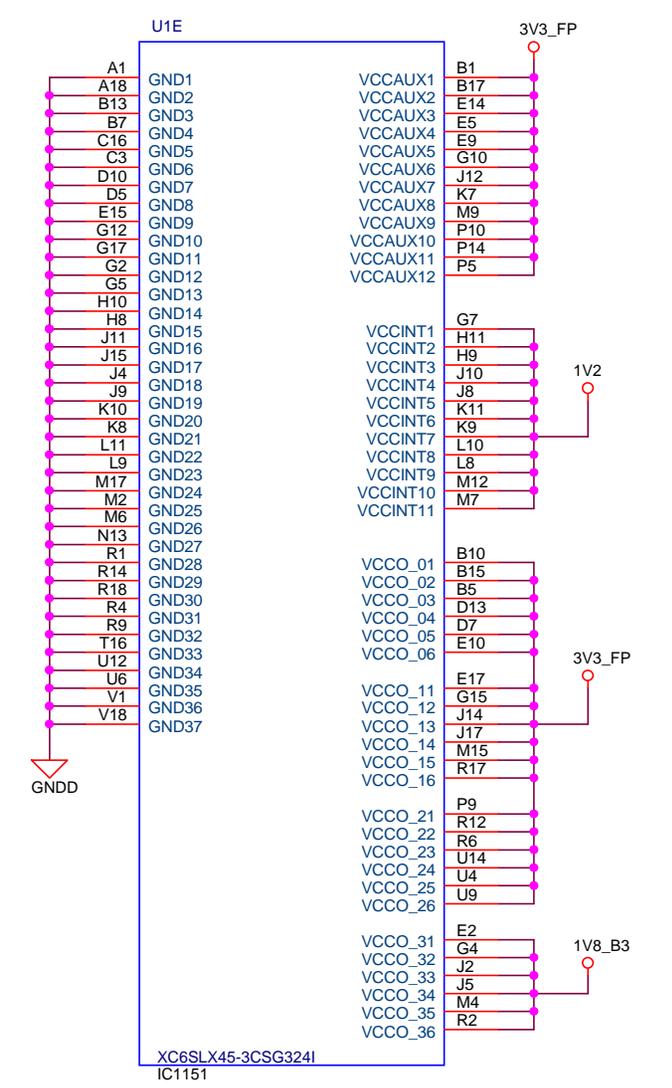
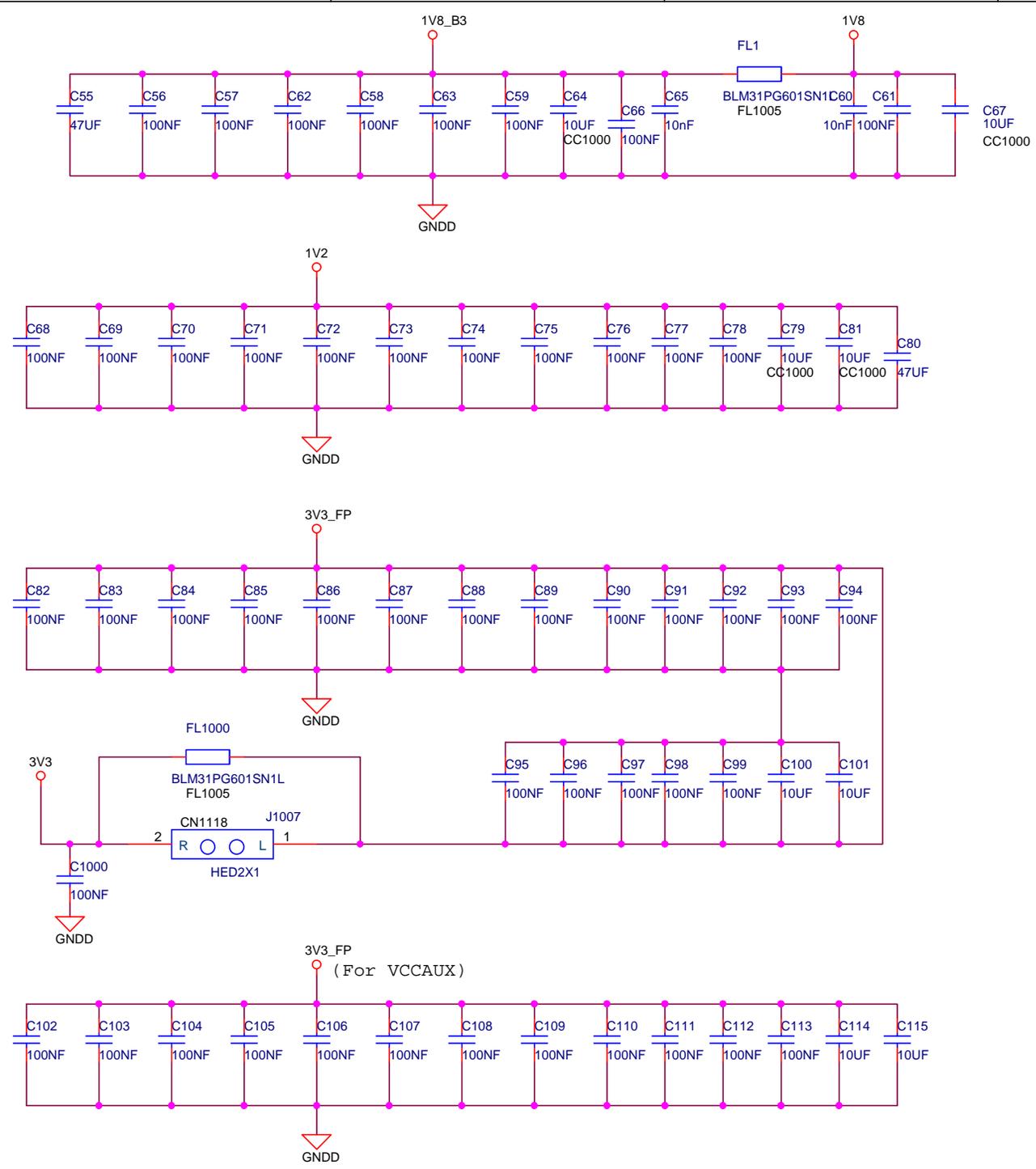


Use PD for USB JTAG II
Use PD for legacy USB JTAG

Short J1000 to enable chip scop

(Switch between 0-ohm and DNA for SPI download)

Title		FPGA CONFIGURATION		IPCLOCK	Rev 2.1
Size B	Document Number	AS00IPC600			
Date:	Tuesday, March 25, 2014	Sheet	6	of	25



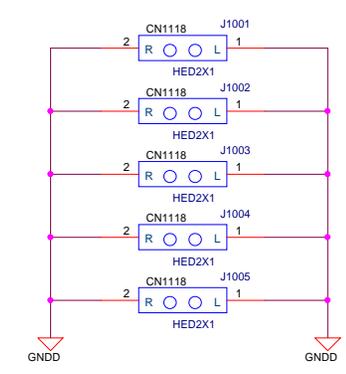
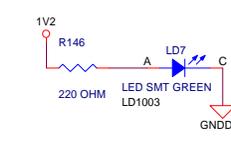
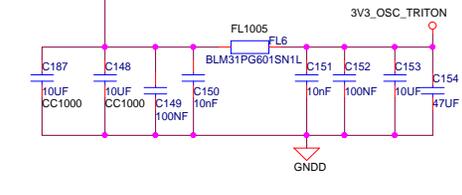
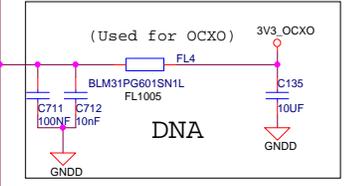
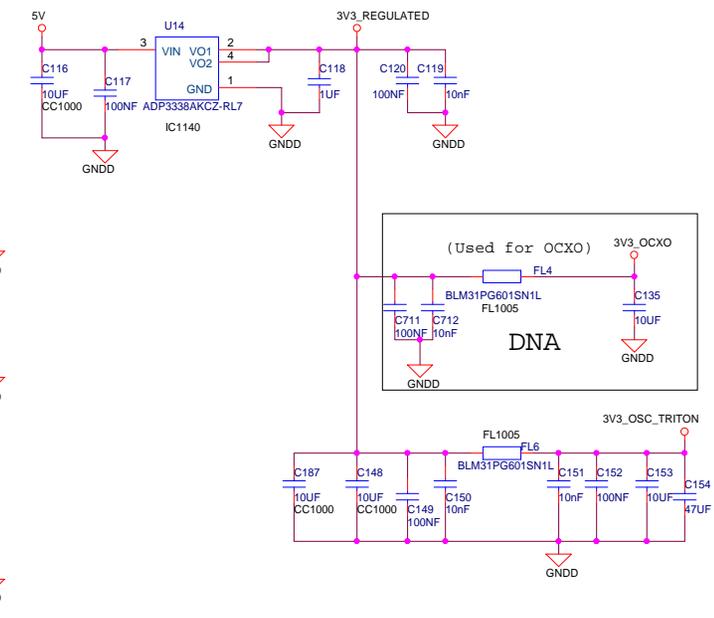
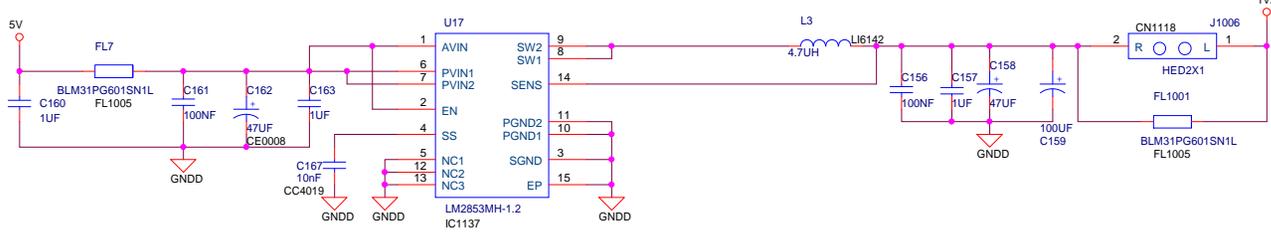
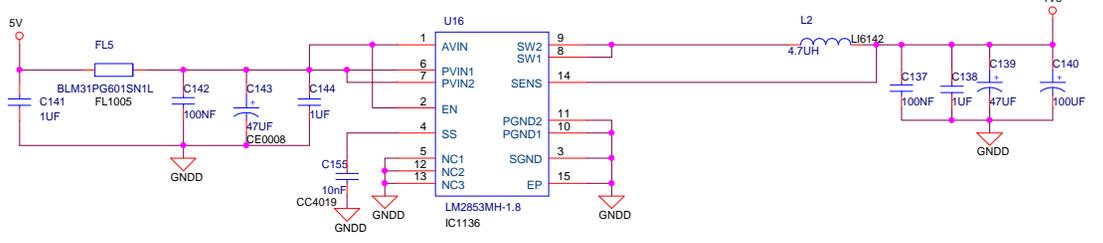
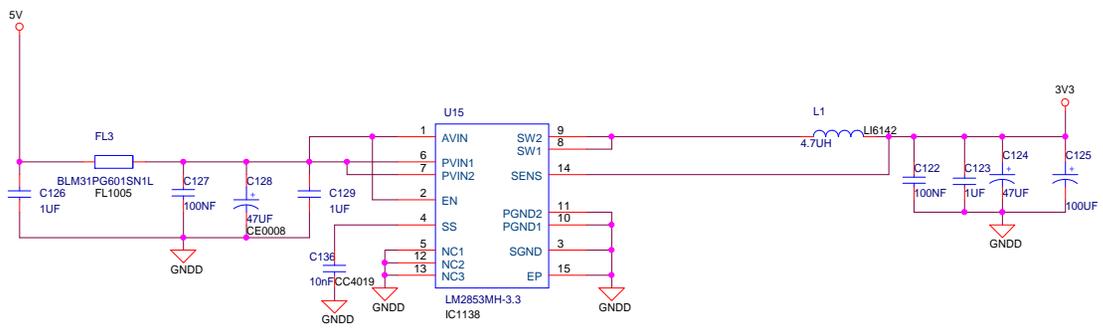
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Size	A4	Document Number	AS00IPC600
Date	Monday, March 24, 2014	Sheet	7 of 25



Rev 2.1

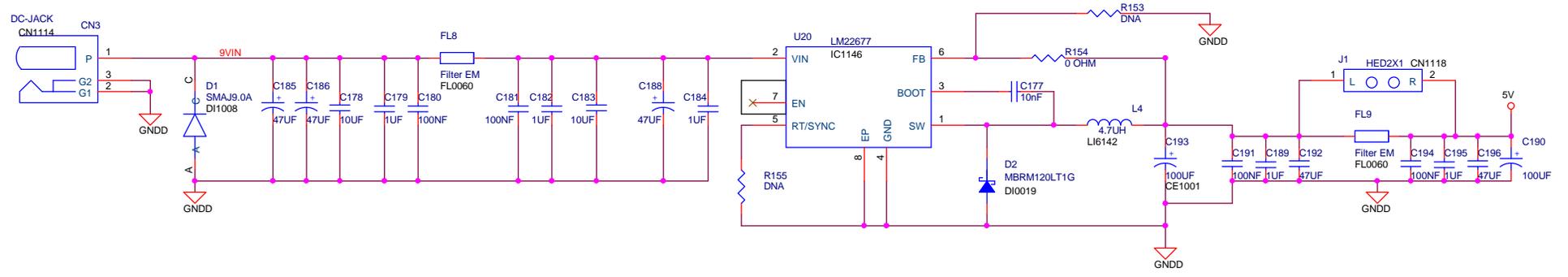
(Accuracy is +/-1.4% over -40,+125c)

1A



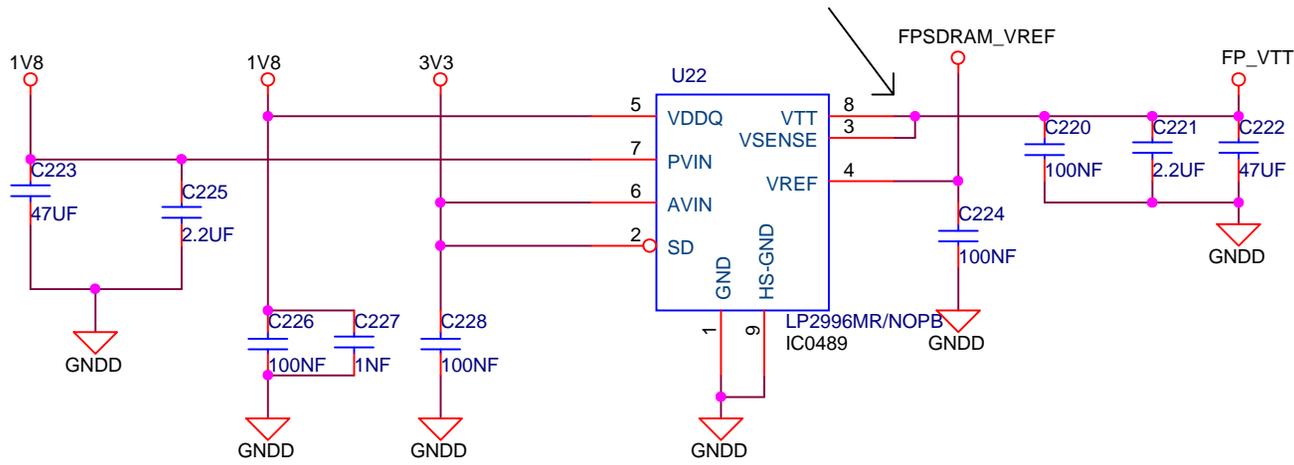
GND TEST POINTS

<Variant Name>			
Title	DC-DC-LOW-VOLTAGES	IPCLOCK	
Size	A3		Document Number
Date:	Tuesday, March 25, 2014	Sheet	8 of 25
Rev	1.0		

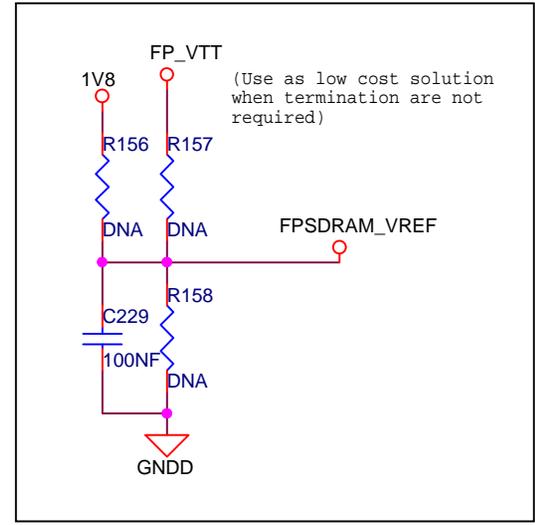


Title		POWER-IN		IPCLOCK	
Size	B	Document Number	AS00IPC600		Rev
Date:	Tuesday, March 25, 2014	Sheet	10	of	25

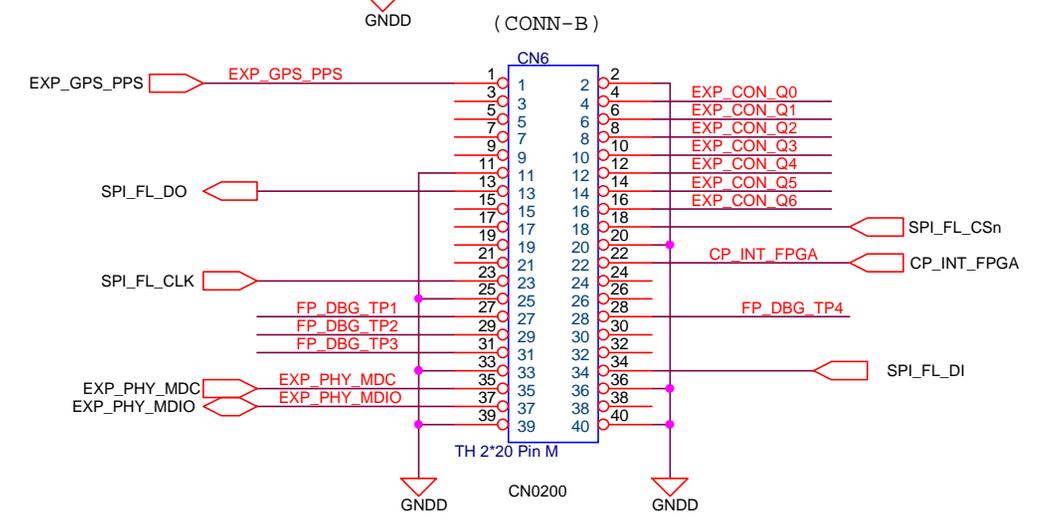
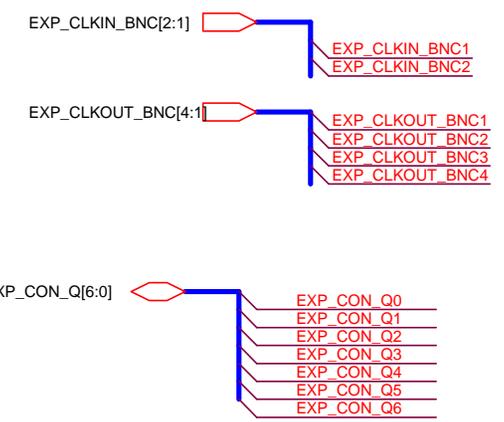
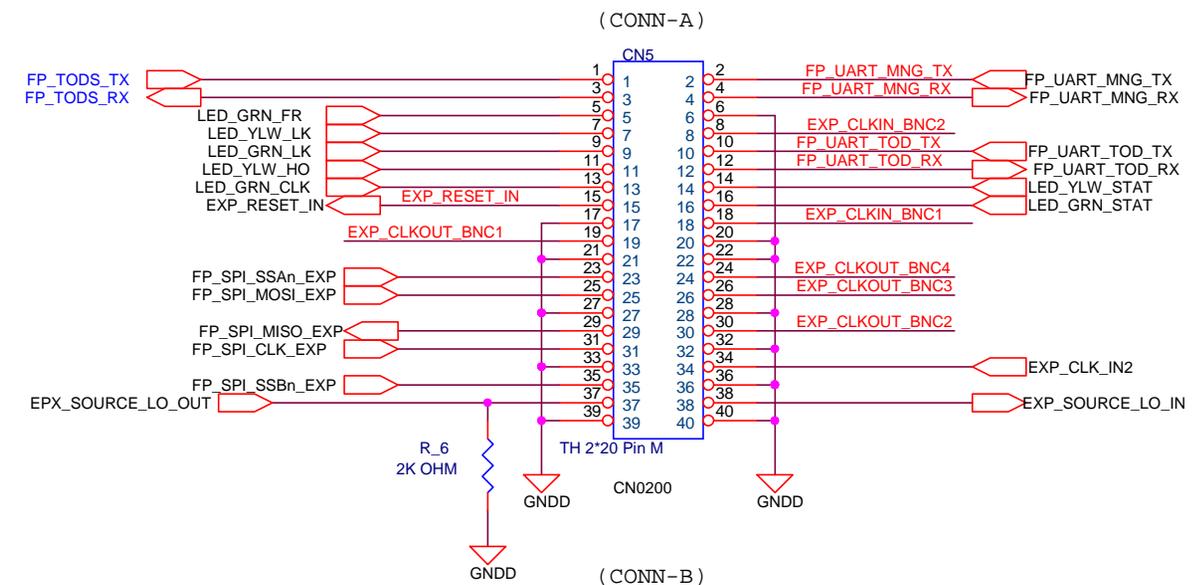
(Connect "Vsense" to the middle of the VTT bus)



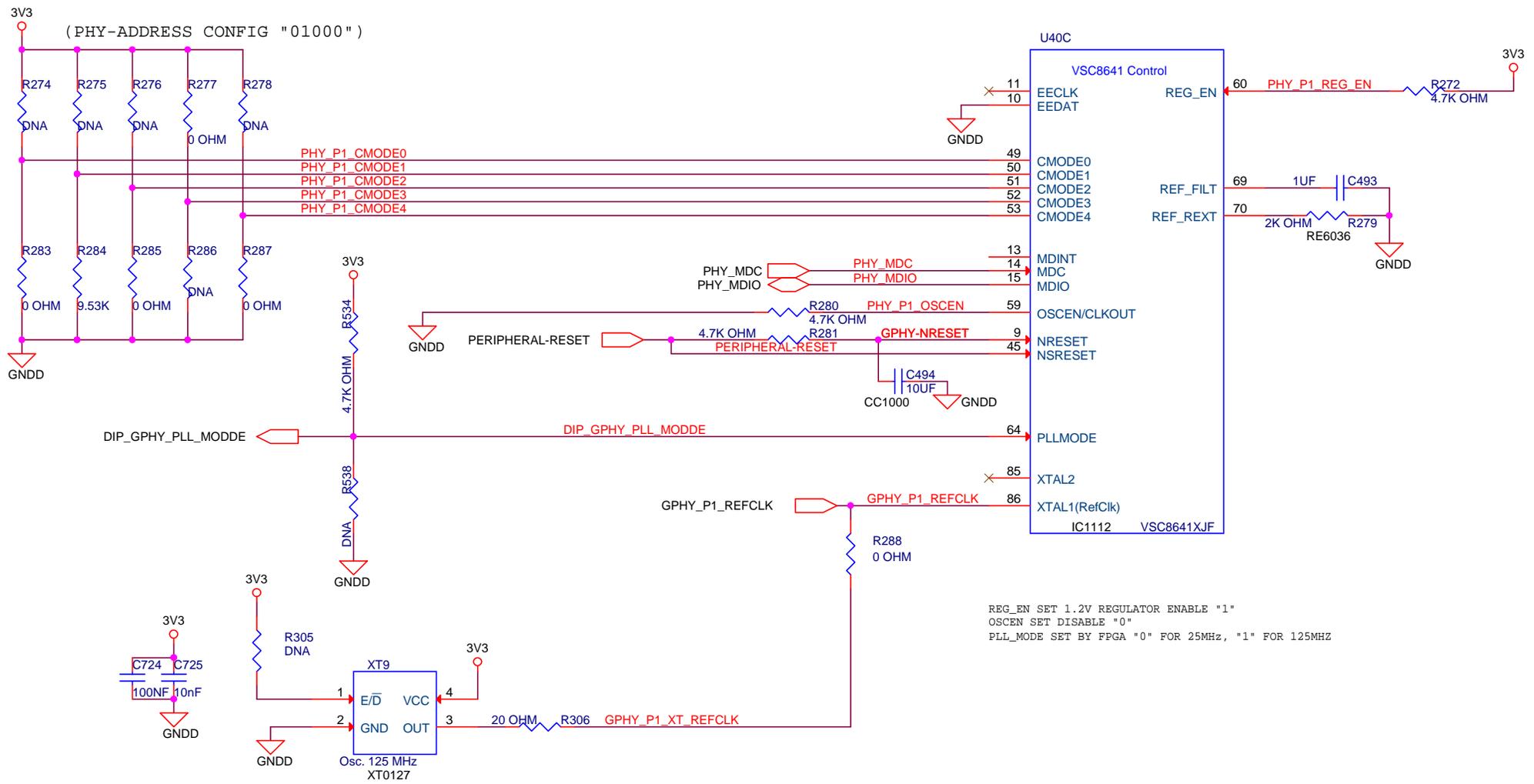
DDR Termination Regulator for FPGA SDRAM



Title		IPCLOCK
TERMINAITON		
Size A	Document Number AS00IPC600	Rev 1.0
Date:	Monday, March 24, 2014	Sheet 11 of 25



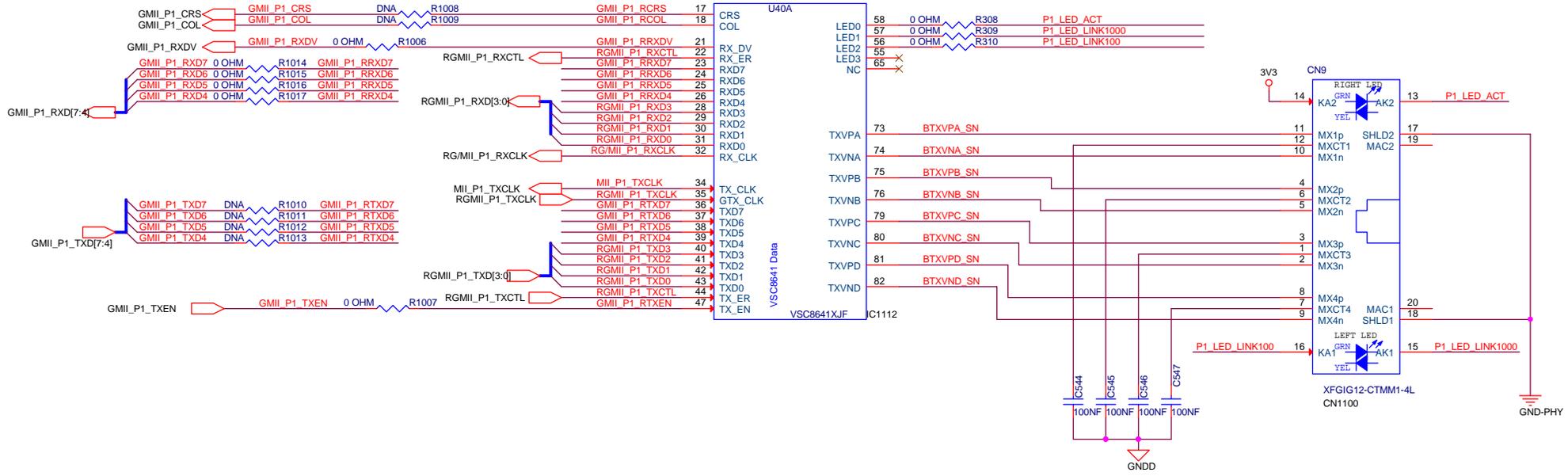
Title		
EXPANSION CONNECTORS		
IPCLOCK		
Size	Document Number	Rev
A4	AS00IPC600	1.0
Date:	Wednesday, March 26, 2014	Sheet 12 of 25



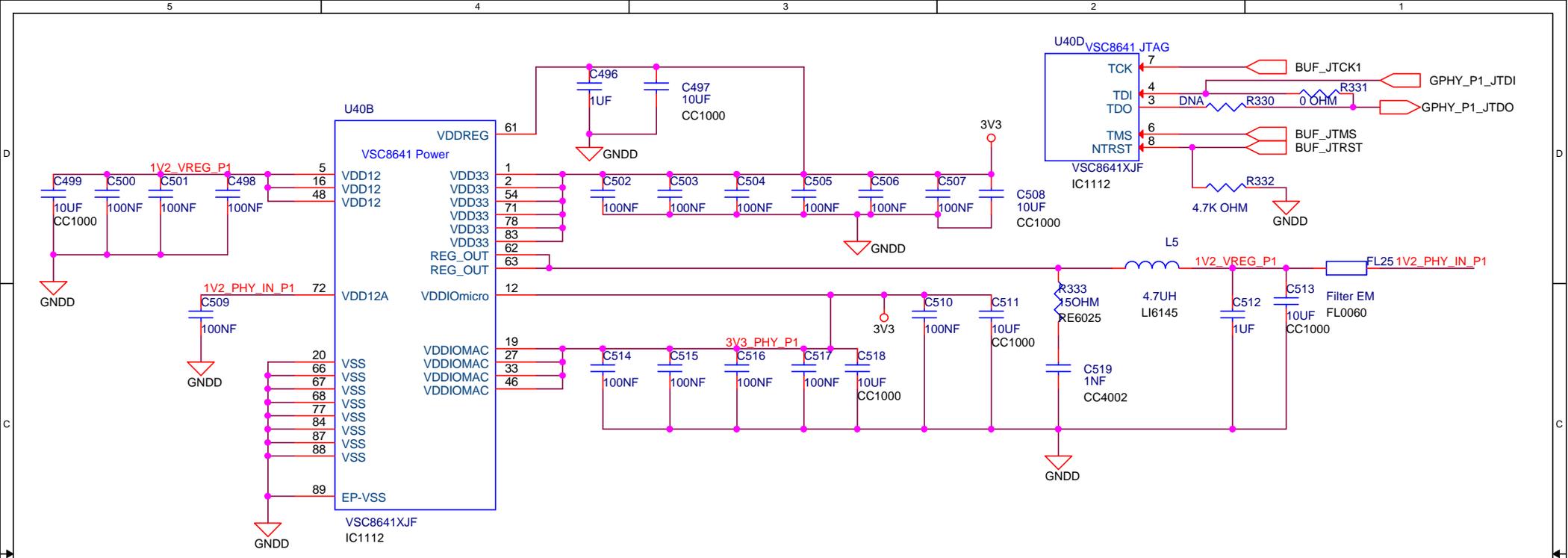
When XT9 is not used-
 Don't assembly R306, R288

Title		GIGA_PHY_CONF		IPCLOCK
Size	A4	Document Number	AS00IPC600	
Date:	Wednesday, March 26, 2014	Sheet	13 of 25	Rev 2.1

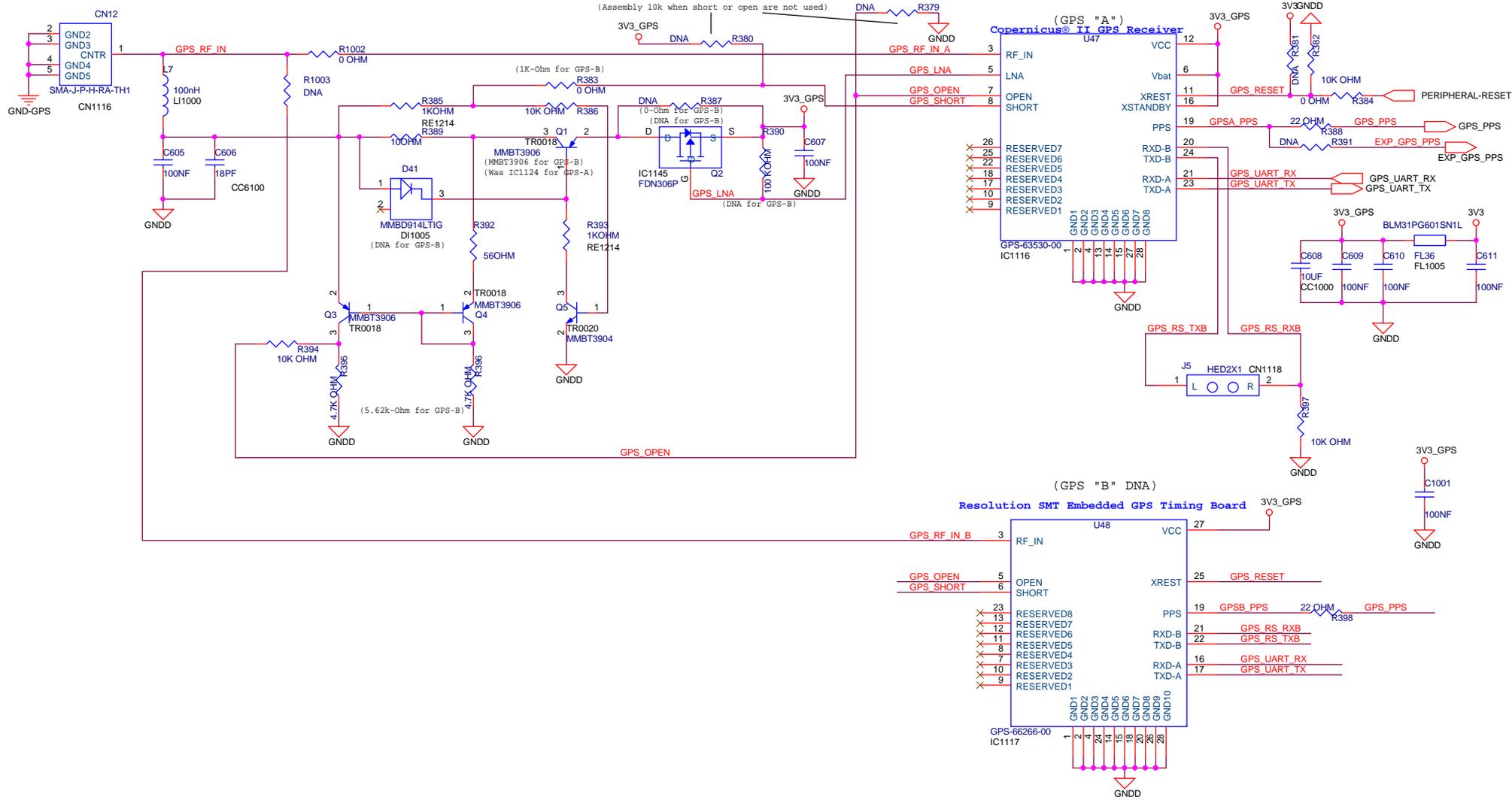
SIGNAL NAME	LED COLOR / SIDE	ACTIVE
ACTIVE	GREEN RIGHT	P1_LED_ACT='0'
LINK100	GREEN LEFT	P1_LED_LINK100='1'
LINK1000	YELLOW LEFT	P1_LED_LINK100='0'
		P1_LED_LINK1000='1'



Title		GIGA_PHY_MAG		IPCLOCK	Rev 1.0
Size B	Document Number	AS00IPC600			
Date:	Tuesday, March 25, 2014	Sheet	14	of	25

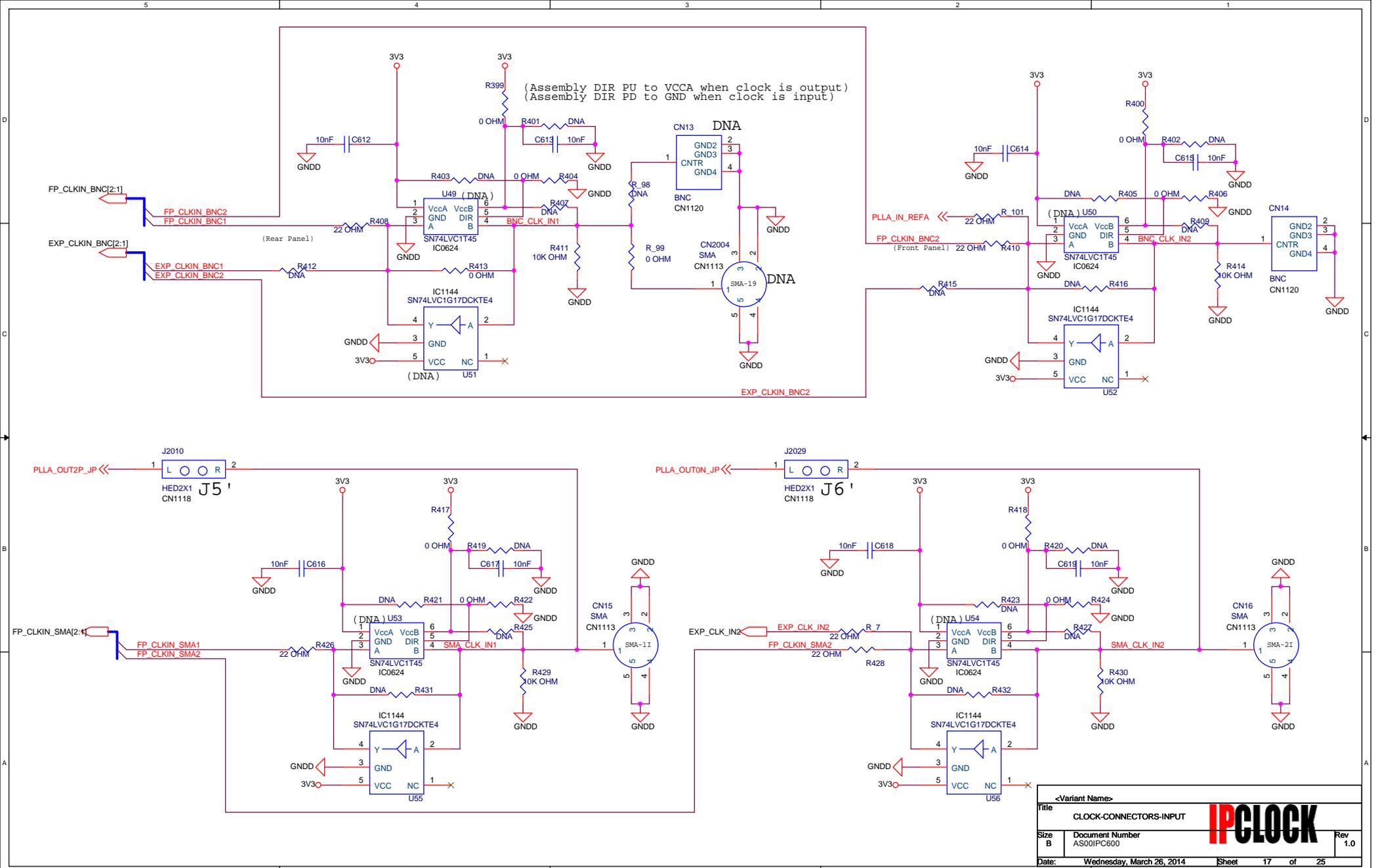


Title		GIGA_PHY_POWER		IPCLOCK
Size	A4	Document Number	AS00IPC600	
Date:	Tuesday, March 25, 2014	Sheet	15 of 25	Rev 1.0

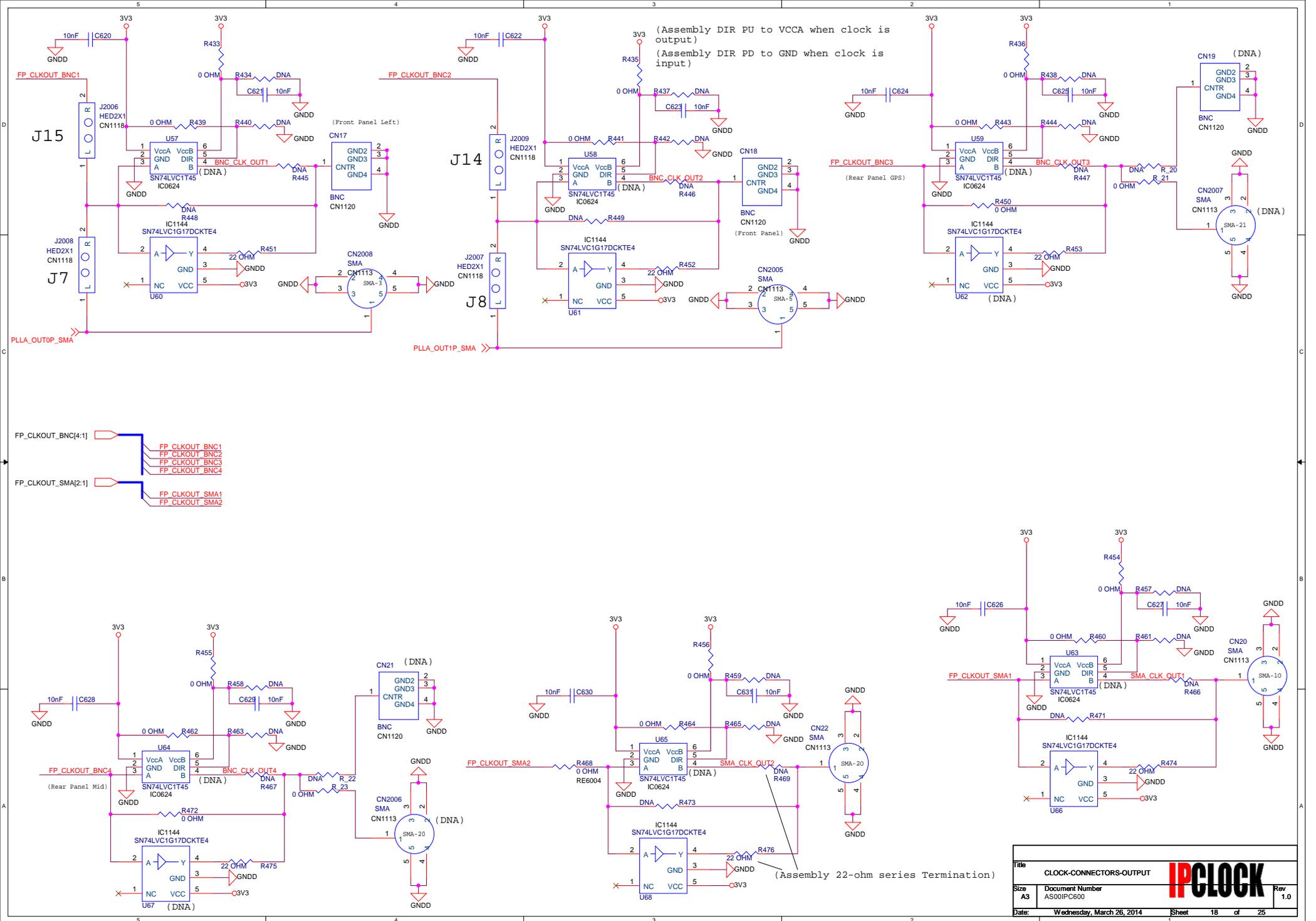


Title		GPS	
Size		B	
Document Number	AS00IPC600	Rev	1.0
Date:	Monday, March 24, 2014	Sheet	16 of 25

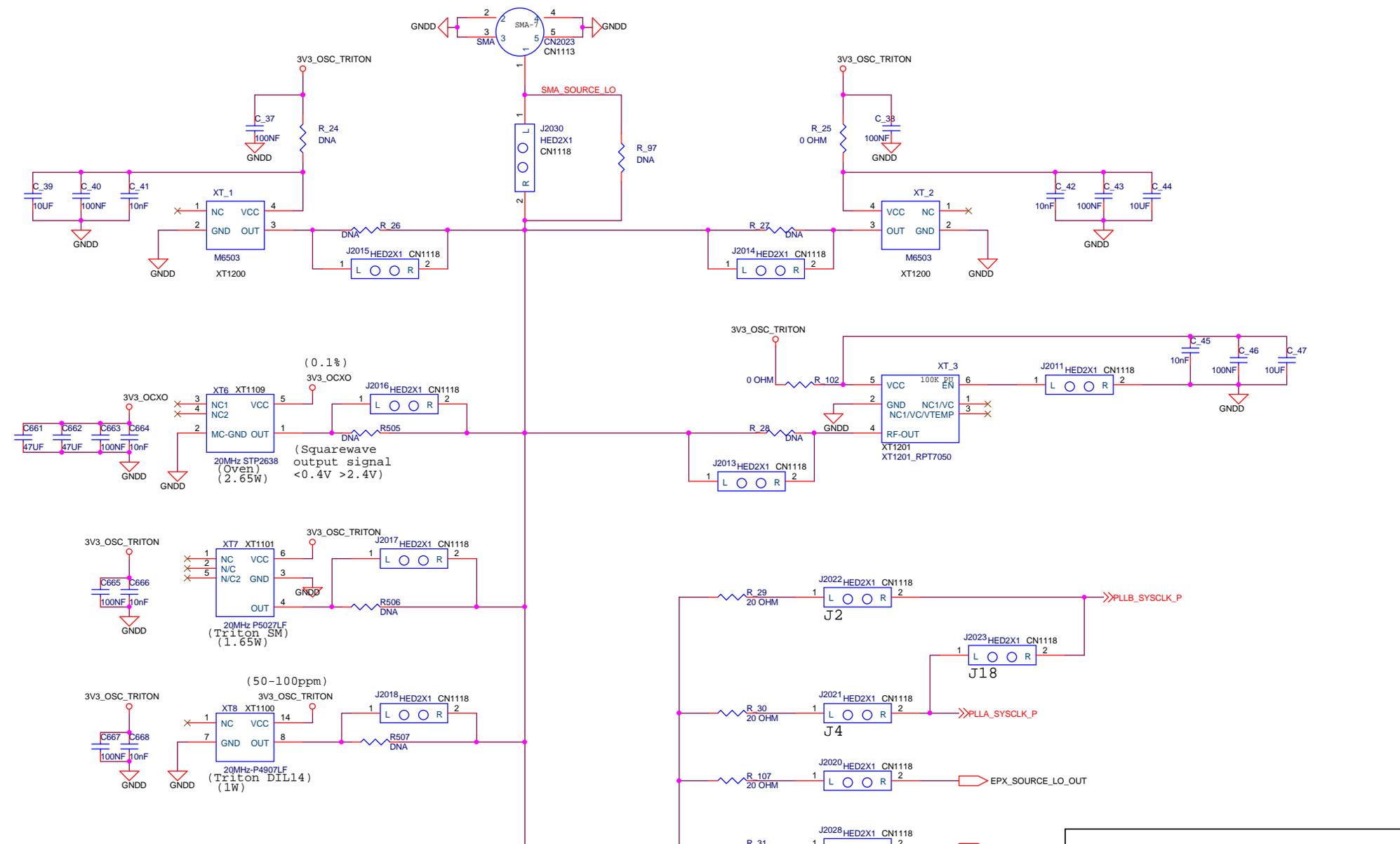




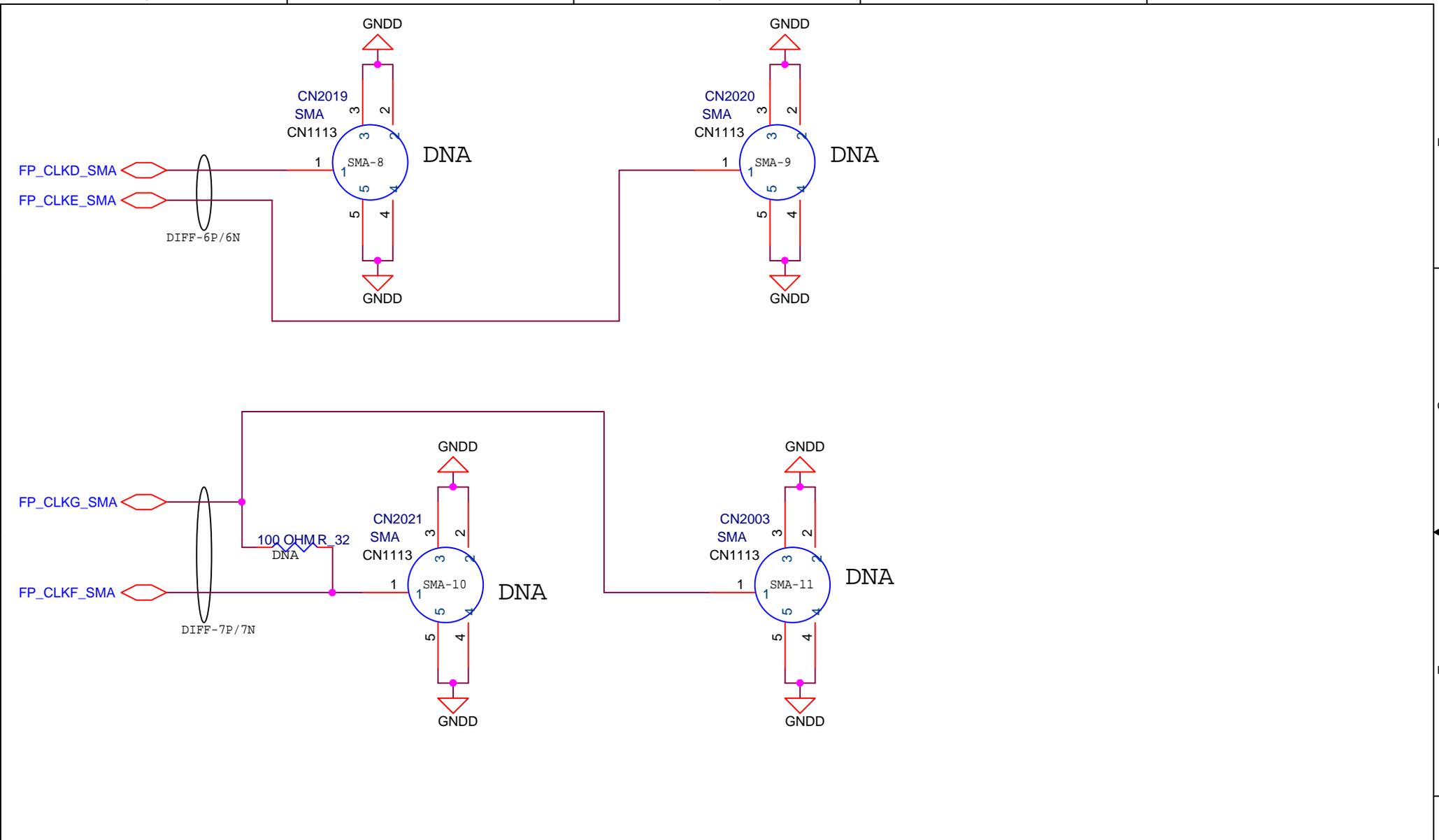
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Size B	Document Number	AS00IPC600			
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Title		CLOCK-CONNECTORS-OUTPUT		IPCLOCK	Rev 1.0
Size	A3	Document Number	AS00IPC600		
Date:	Wednesday, March 26, 2014	Sheet	18 of 25		

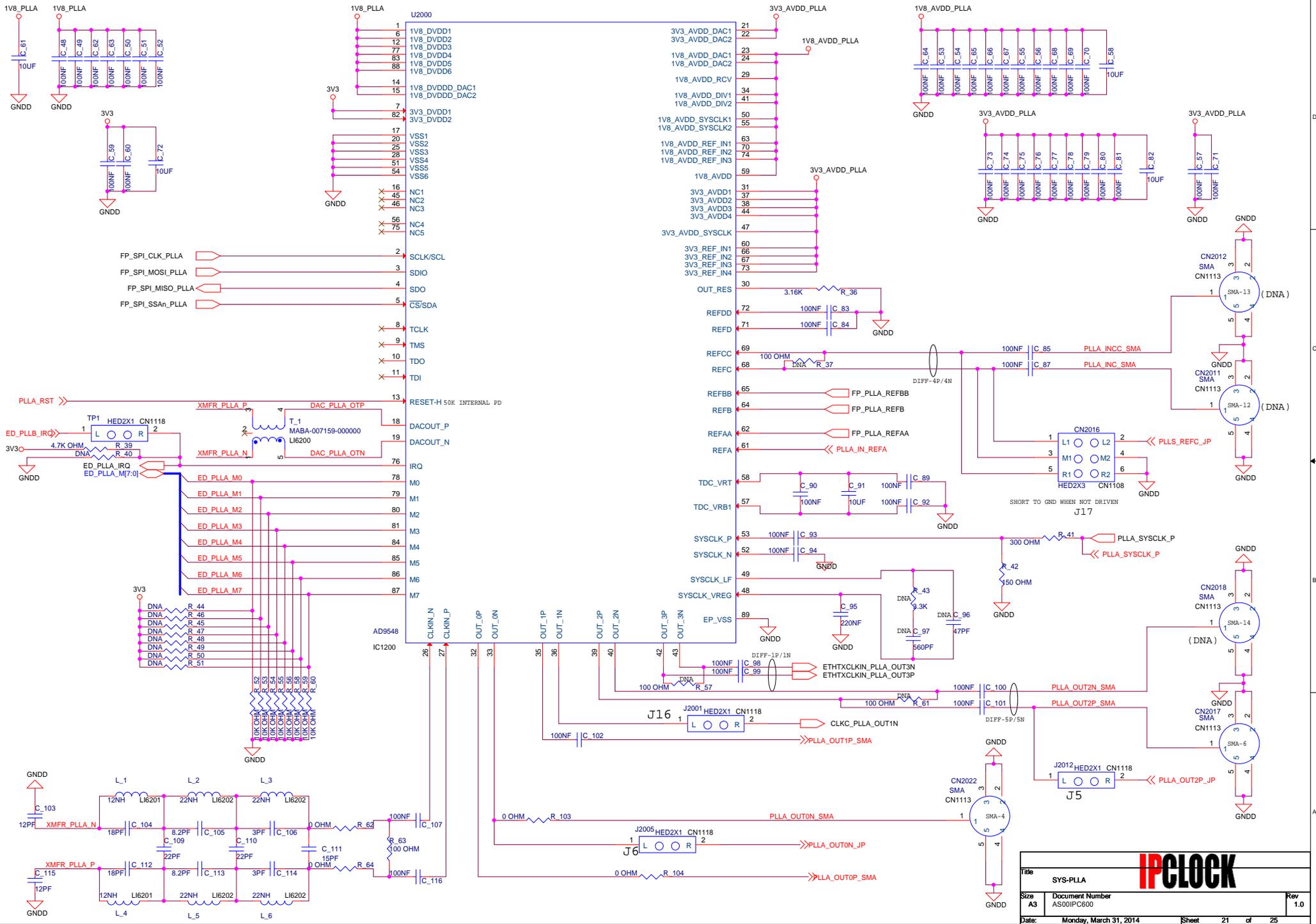


Title		CLOCK-SOURCE		IPCLOCK
Size	B	Document Number	AS00IPC600	
Date:	Monday, March 31, 2014	Sheet	19 of 25	Rev 1.0



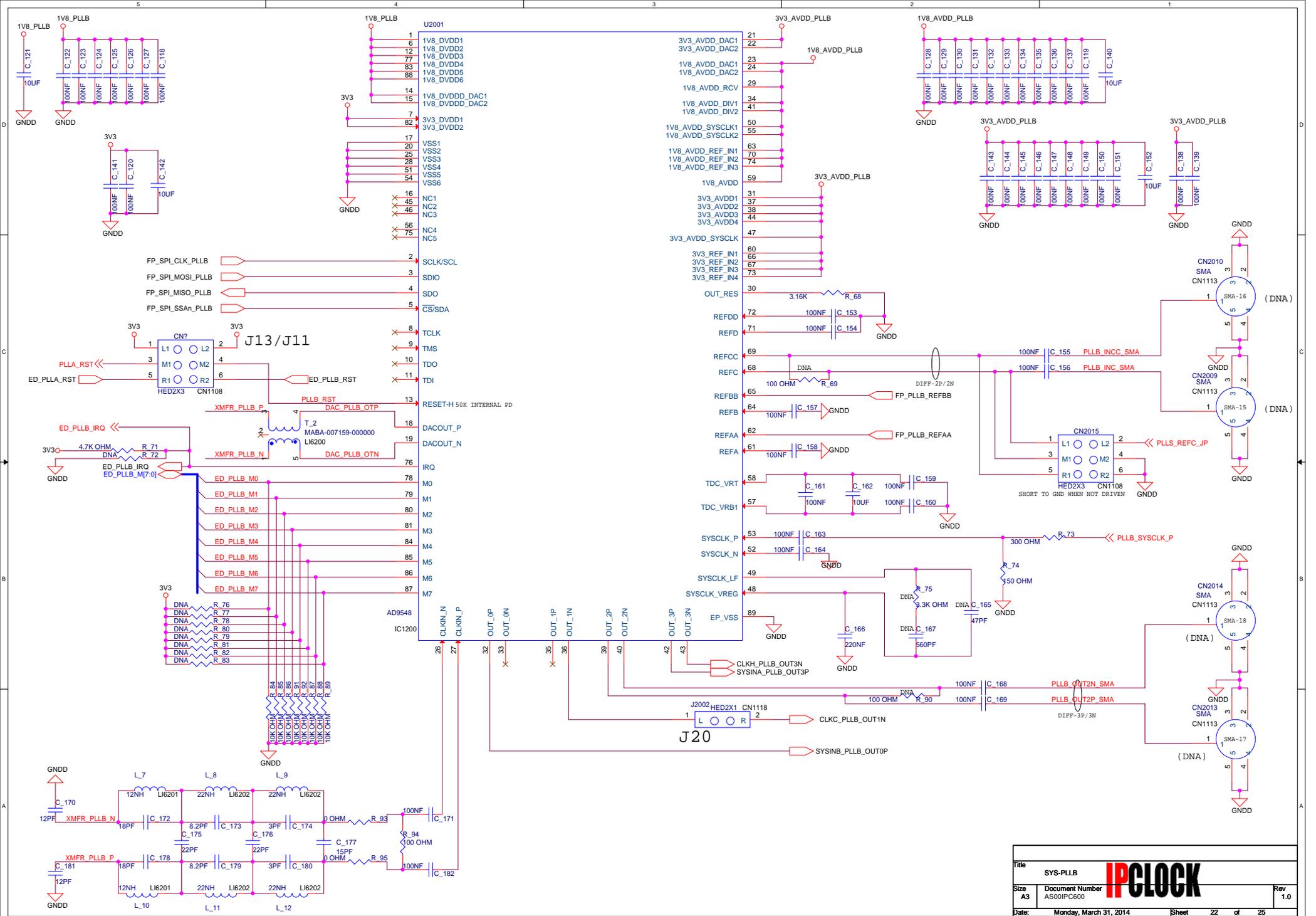
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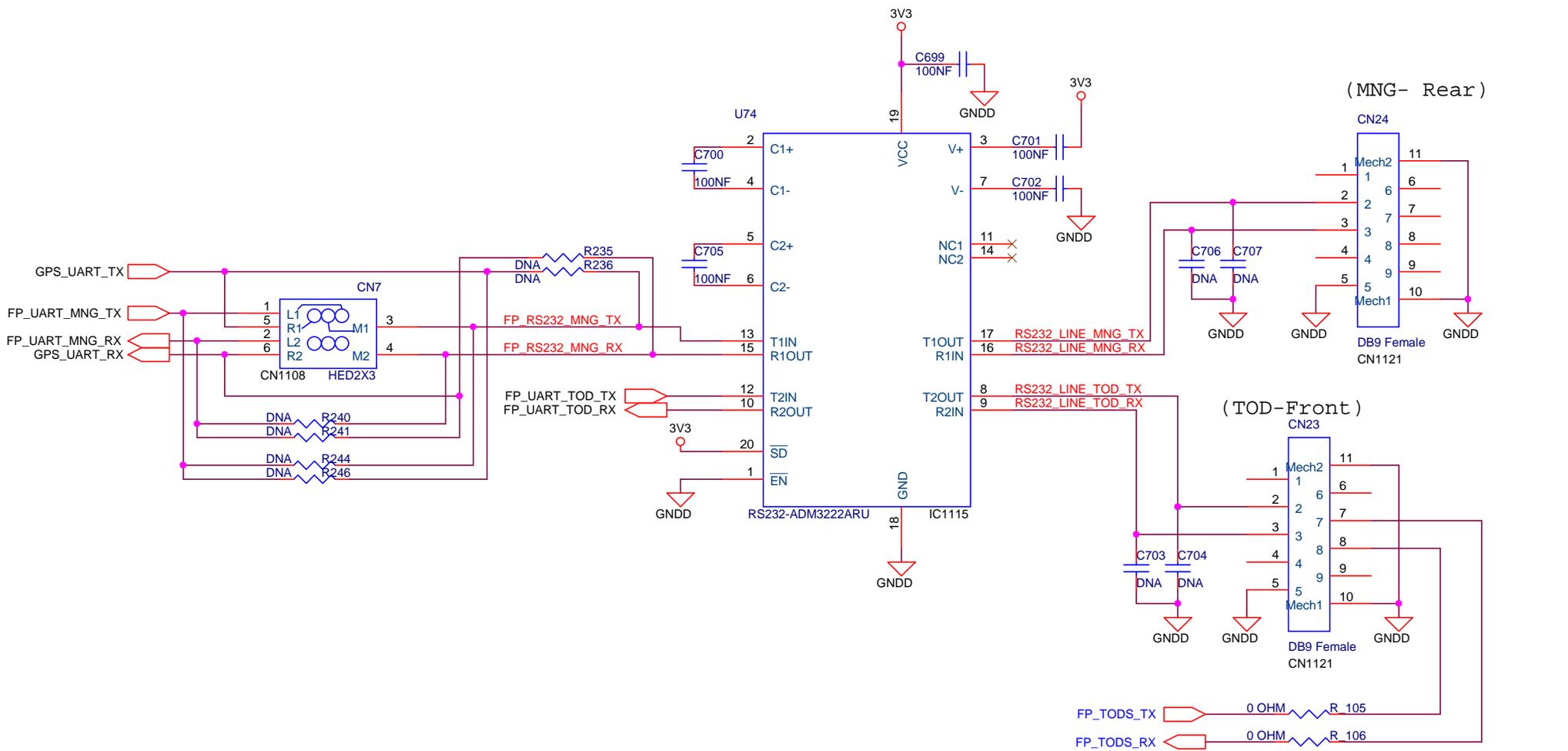
Title		
SMA		IPCLOCK
Size	Document Number	
A	AS00IPC600	Rev 1.0
Date:	Wednesday, March 26, 2014	Sheet 20 of 25



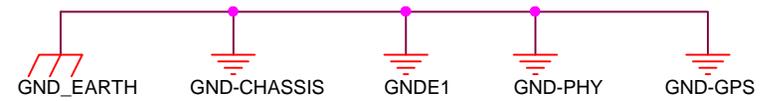
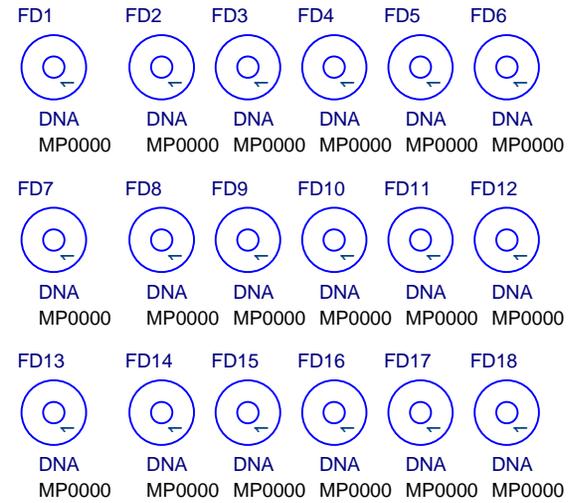
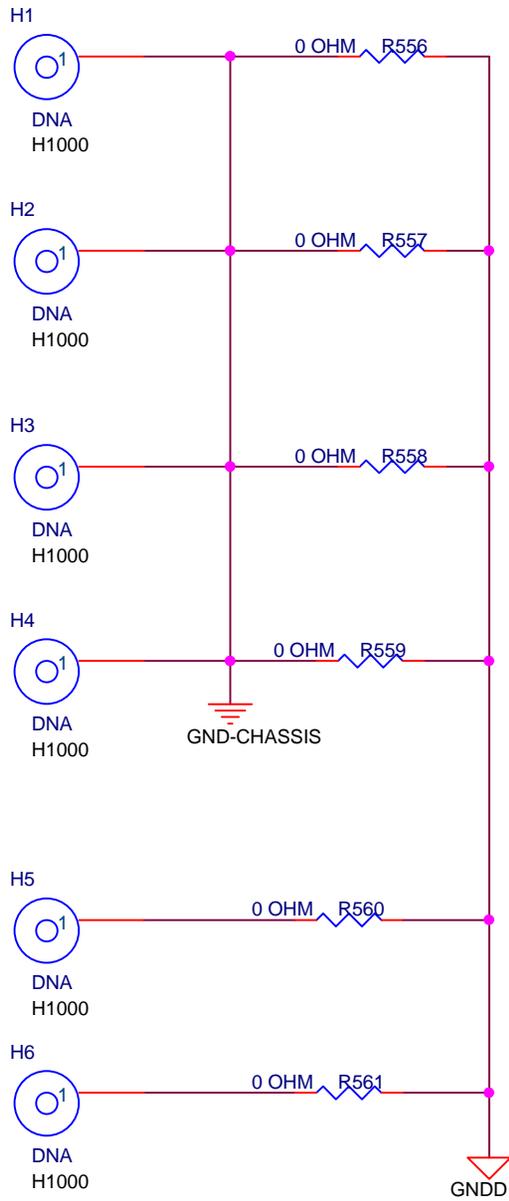
Title		SYS-PLLA	
Size		Document Number	
A3	AS00PC600	Rev 1.0	
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Title		COM		IPCLOCK	Rev 1.0
Size A4	Document Number AS00IPC600				
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Title		IPCLOCK	
MECH			
Size	Document Number	Date:	Rev
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Date:		Monday, March 24, 2014	Sheet 24 of 25
		2	1

