

Product	Type	Slave Clock	Master Clock	BC	Stand alone	# of ch	Ethernet I/F	Memory	Xilinx FPGA	EVB
<b>IPC1603</b>	Chip on FPGA	✓			✓	1	RGMII	DDR2	XC6SLX45	IPC500 IPC600
<b>IPC1710</b>	Chip on FPGA	✓	✓		✓	64	RGMII	DDR2	XC6SLX45	IPC500 IPC600
<b>IPC9000</b>	Chip on FPGA	✓	✓	✓	✓	64	RGMII	DDR2	XC6SLX45	IPC500 IPC600
<b>IPC9100</b>	Chip on FPGA	✓	✓	✓	✓	128	SGMII	DDR3	XC7Z7010	IPC505 ZC706
<b>IPC9820</b>	SoC	✓	✓		✓	64	RGMII	DDR2	XC6SLX45	IPC500 IPC600
<b>IPC9830</b>	SoC	✓	✓	✓	✓	64	RGMII	DDR2	XC6SLX45	IPC500 IPC600
<b>IPC9840</b>	SoC	✓	✓	✓	✓	128	SGMII	DDR3	XC7Z7010	IPC500 IPC600
<b>IPC9004</b>	IP Core	✓	✓	✓		64	GMII	DDR3	Series-7	KC705
<b>IPC9006</b>	IP Core	✓	✓	✓		64	GMII	DDR2	Spartan-6	IPC500 IPC600
<b>IPC9010</b>	IP Core	✓	✓	✓		64	GMII	DDR3	XC7Z7000	ZC706
<b>IPC500</b>	Evaluation kit	✓	✓	✓						
<b>IPC505</b>	Evaluation kit	✓	✓	✓						
<b>IPC600</b>	Evaluation kit - Demo equipment	✓	✓	✓						